

Figure 1

PCI Bus
Architecture
100

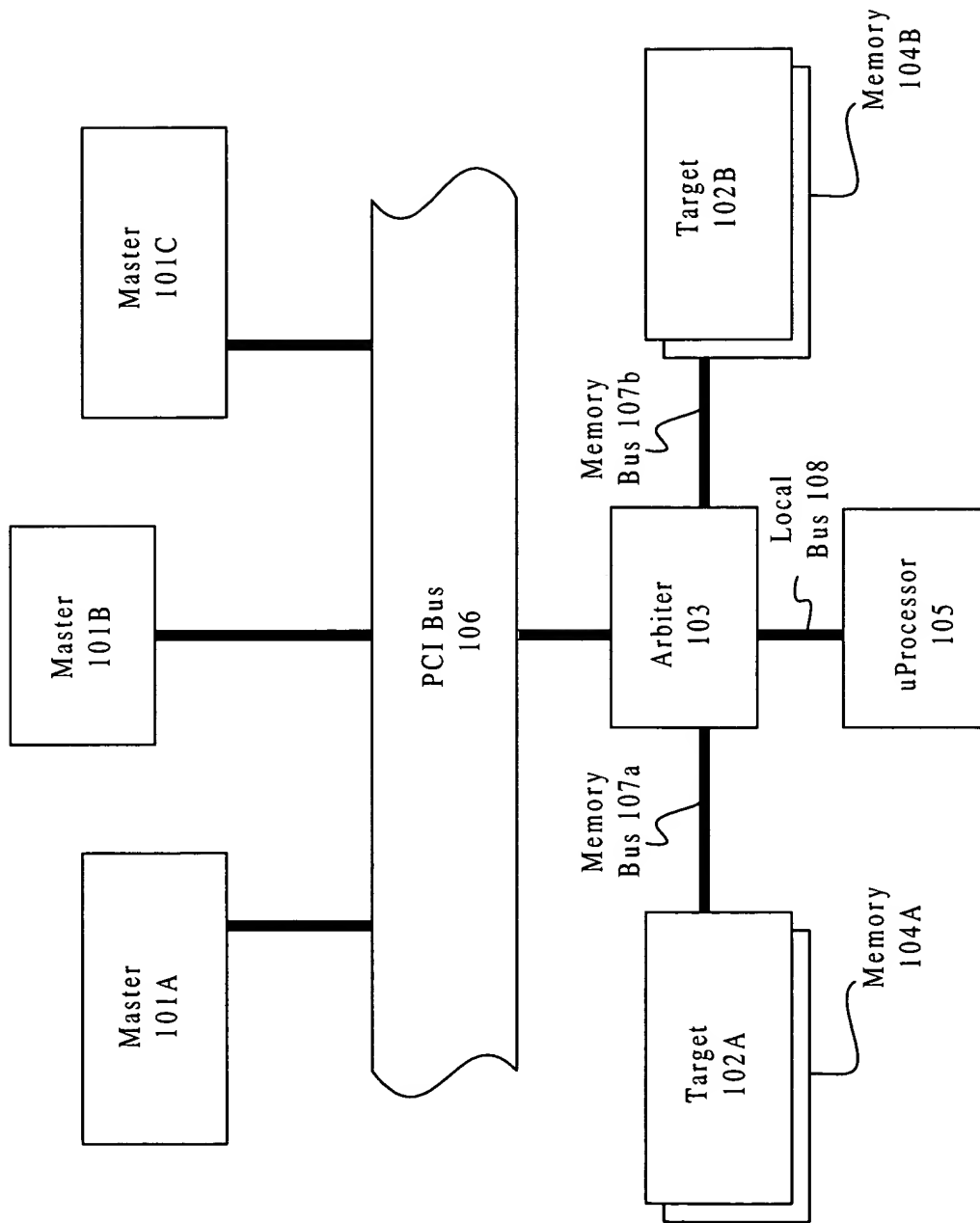
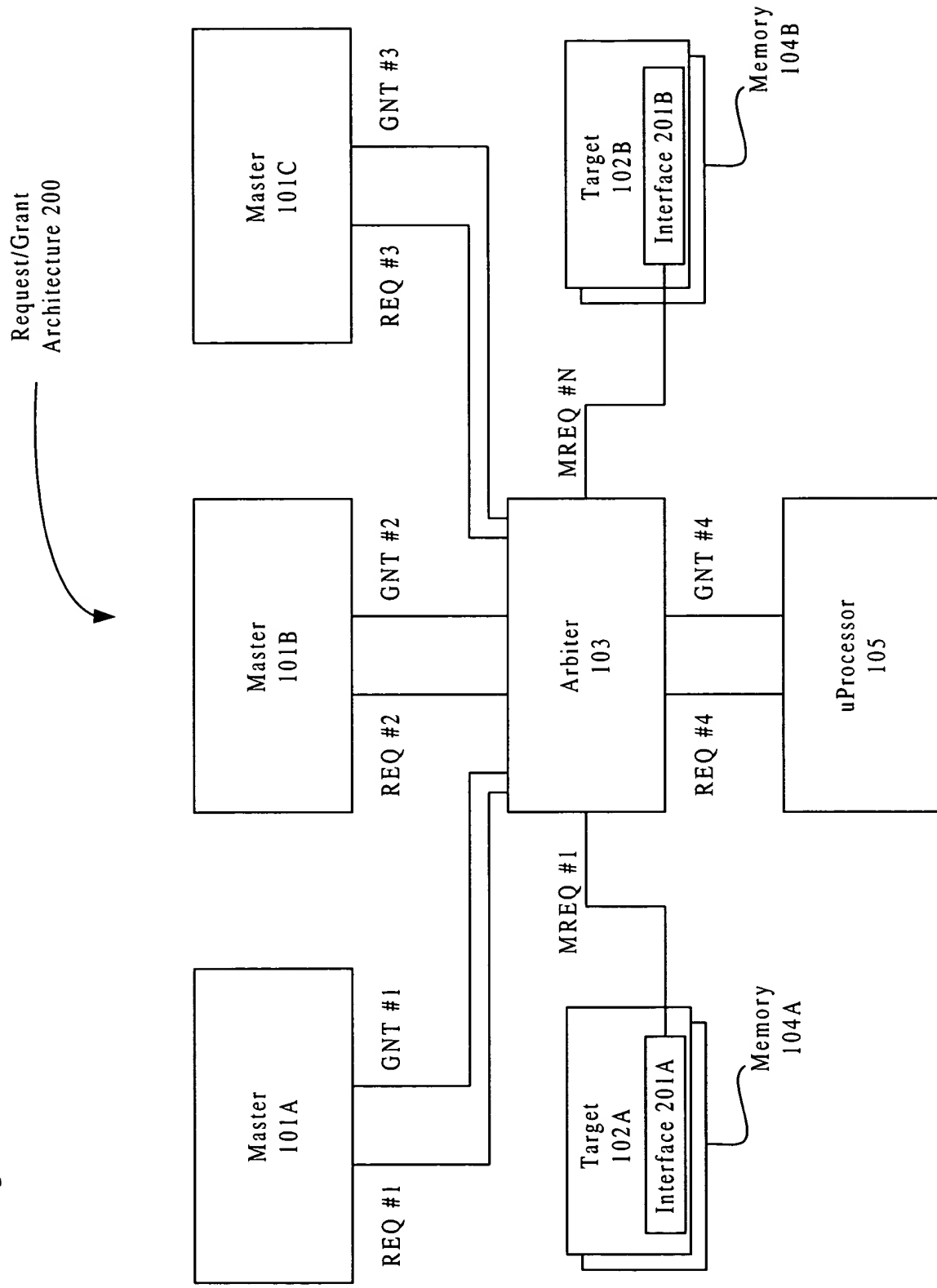


Figure 2



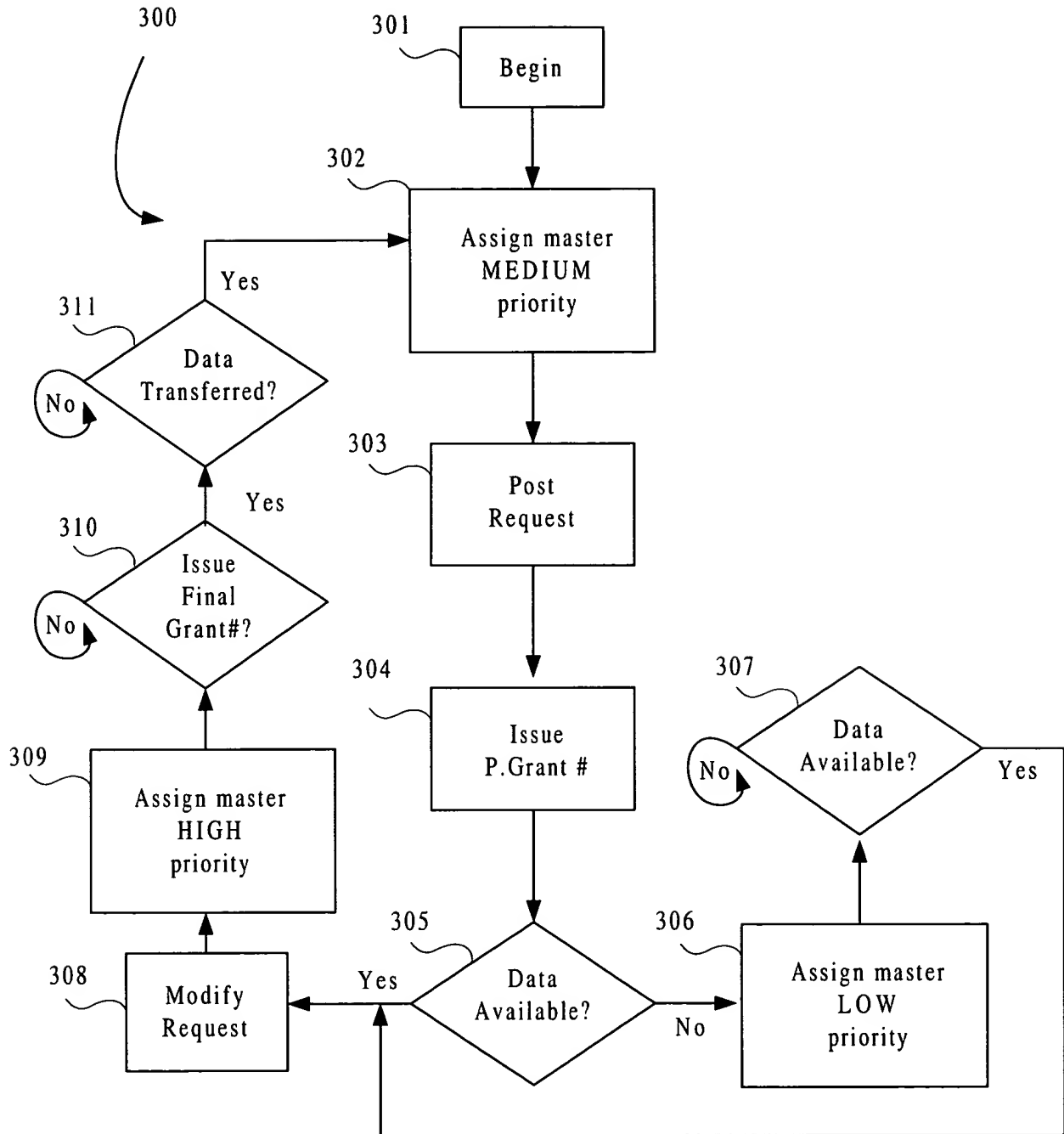


Figure 3

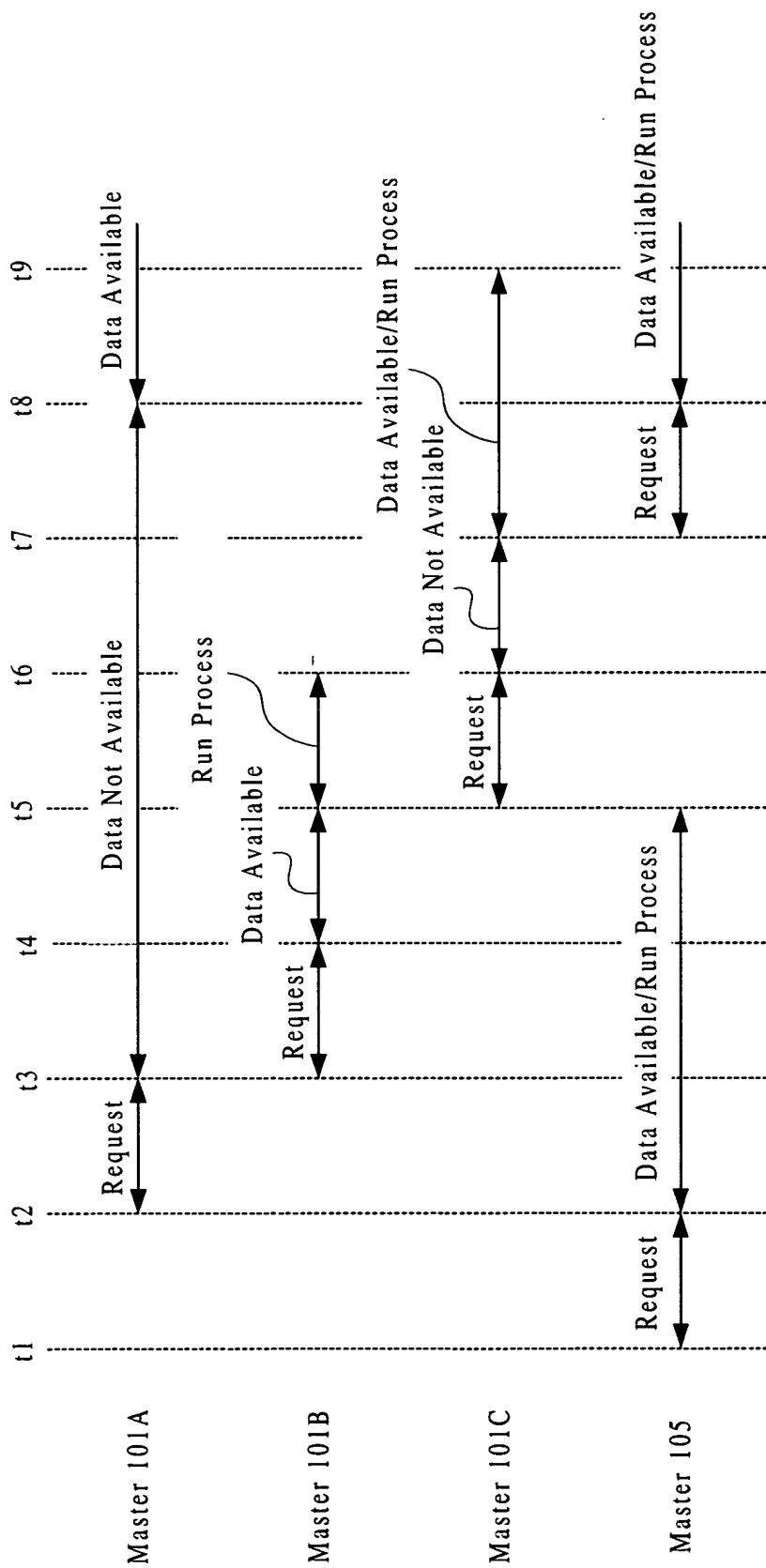


Figure 4

500 

Request Information 501A	Master ID 502A	Data 503A
Request Information 501B	Master ID 502B	Data 503B

Figure 5